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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/011,018	12/04/2001	3762	920	VTO322- US1	5	30	3

CONFIRMATION NO. 6936

24473
STEVEN M MITCHELL
PACESETTER INC
701 EAST EVELYN AVENUE
SUNNYVALE, CA 94086

FILING RECEIPT



OC000000007307768

Date Mailed: 01/14/2002

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Applicant(s)

Louis Wong, Santa Clara, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 01/14/2002

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Methods and devices for reducing integrated circuit power consumption in implantable cardiac stimulation devices

Preliminary Class

607

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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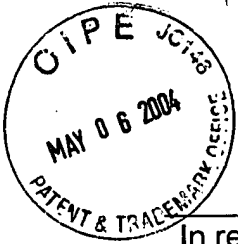
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Patent
VT0322-US1

3762

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Date: May 3, 2004
Wong	:	
Serial No: 10/011,018	:	Group Art Unit: 3762
Filed: December 14, 2001	:	
For: "METHODS AND DEVICES FOR REDUCING INTEGRATED CIRCUIT POWER CONSUMPTION IN IMPLANTABLE CARDIAC STIMULATION DEVICES"	:	Examiner: unknown

REQUEST FOR STATUS

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It has been over two years since the above-captioned application was filed. A copy of the filing receipt is enclosed.

Please notify the undersigned with the current status of this application.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited
in the United States Postal Service as 1st Class mail in an
envelope addressed to the Honorable Commissioner
for Patents, Mail Stop: Status Inquiry, P. O. Box 1450
Arlington, VA 22313-1450 on May 3, 2004.